

# United States Patent [19]

Schneider

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### [54] TORPEDO SIGNAL PROCESSOR

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[73] Assignee: The United States of America as

represented by the Secretary of the Navy, Washington, D.C.

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23; 364/423, 462, 516

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# [57] ABSTRACT

An integrated torpedo sonar signal processor having an integrated analog-to-digital conversion component is provided. The torpedo sonar signal processor has four groups of low-pass filtered analog-to-digital converters, each group containing thirteen converters. The output of a group (thirteen converters) is a serial data signal which is outputted to a field programmable gate array which, in turn, converts the combined signal to parallel data word. The parallel data word is outputted to a first digital signal processor which in turn outputs to a second dual-port digital signal processor. the processor providing prefiltering and space-time processing. The output signal is then sent to a beamformer dual-port digital signal processor which has an attached external memory. The beamformer signal is then outputted to a signal processor component containing four dual-port digital signal processors and a common external memory. The entire torpedo sonar signal processor is controlled by a network controller which sequences the program downloads and system initialization. The network controller also provides a link to the torpedo's control processor over a field programmable gate array-based interface.

## 12 Claims, 6 Drawing Sheets

